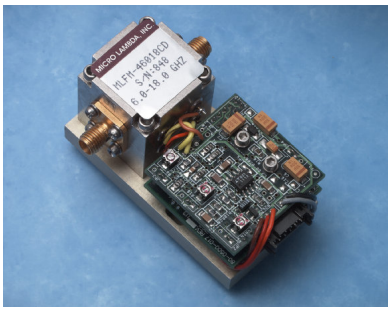
MICRO LAMBDA WIRELESS, INC.

MINIATURE YIG FILTERS WITH COMMERCIAL SERIAL DRIVERS SD SERIES



FEATURES

- 500 MHz to 18 GHz
- Compensation for Temperature Drift
- Low-Profile Package
- Input Regulators for Improved Stability
  - Versus Power Supply Variations
- 16 Bit Tuning Resolution

DESCRIPTION

MICRO LAMBDA YIG Filters, model types MLFI Series and MLFM-series are available with integrated serial driver circuits.

MICRO LAMBDA drivers eliminate the need for customers to design or develop their own driver circuits and sophisticated test and alignment procedures. Integrating a driver at MICRO LAMBDA's factory ensures that peak performance will be achieved at the time of manufacture. Alignment and compensation with the particular YIG filter can be maximized down to the component level.

All drivers in this series provide input voltage regulators, and compensation circuits to improve frequency drift.

STANDARD POSITIVE INPUT DIGITAL DRIVER SELECTION GUIDE: SD SERIES

COMMERCIAL SERIAL DRIVERS	.5-18 GHz YTOs, SD SERIES
DRIVER INPUT & RESPONSE	SPECIFICATION (0 to +65 deg. C)
Tuning Command	Start Word (all 0's) = Lowest Frequency Stop Word (all 1's) = Highest Frequency
Tuning Resolution	16 BIT Positive Logic (Fmax-Fmin)/65,535 Bit Resolution
Tuning Accuracy (excluding hysteresis)	See Table
Tuning Speed	5 mS for 1 GHz step to within ±10 MHz.
Main Driver Inputs	
Supply Voltage & Current	+15 V ± .5 V @ Filter Tuning Current +50 mA, Max. -15 V ± .5 V @ 50 mA
Supply Voltage Pushing	± 100 kHz, Max. @ ± .5 Vdc
Supply Voltage Ripple	10 mV Ripple Pk-Pk from 2 kHz to 3 MHz
Ground	Chassis Ground
YIG Heater Voltage & Current	+24 Vdc ±4 Vdc @ 300 mA surge for 2 seconds, 25 mA steady state Polarity independent : ±12 Vdc or ±15 Vdc acceptable
Digital Interface	The MLWI digital driver interface is a standard 3-wire connection compatable with SPI/QSPI/MICROWIRE interfaces. The chip-select input (CSELECTn) frames the serial data loading at the data input pin (DATA). Immediately following CSELECTn's high-to-low transition, the data is shifted synchronously and latched into the input register on the rising edge of the serial-clock input (CLOCK). After 16 data bits have been loaded into the serial input register, it transfers its contents to the DAC latch on CSELECTn's low-to-high transition (Figure 2). Note that if CSELECTn does not remain low during the entire 16 CLOCK cycles, data will be corrupted. In this case, reload the DAC latch with a new 16-bit word.

Power-On Reset

The MLWI digital driver has a power-on reset circuit to set the DAC's output to OV(F-min) in unipolar mode when VDD is first applied. This ensures that unwanted DAC output voltages will not occur immediately following a system power-up, such as after power loss.

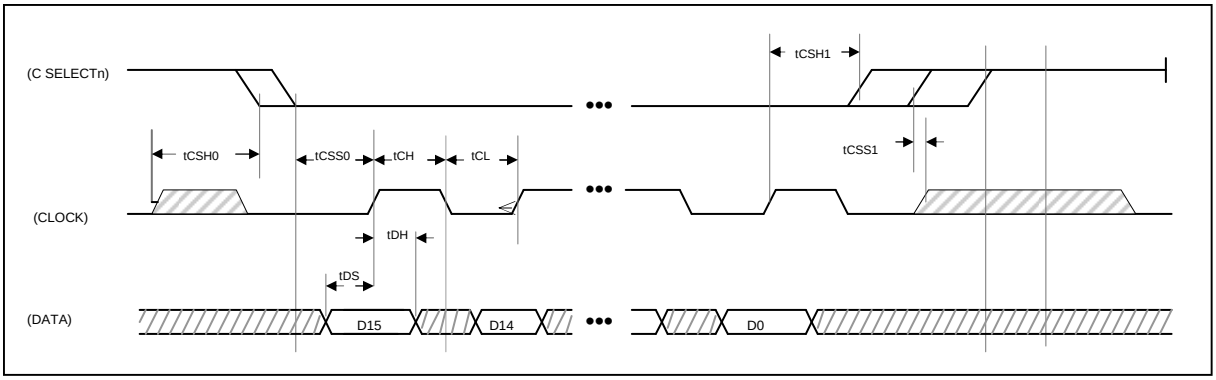


Figure 1. Timing Diagram

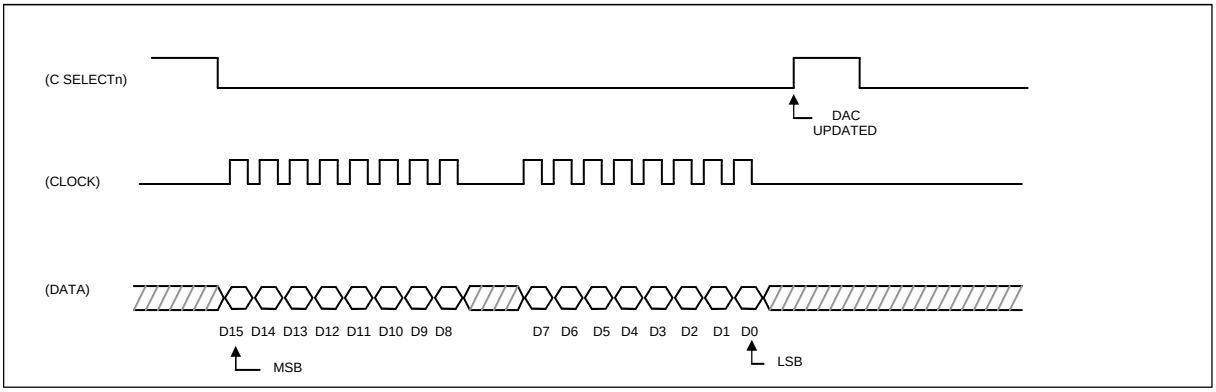


Figure 2. 3-Wire Interface Timing Diagram

TIMING CHARACTERISTICS

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
CLOCK Frequency	fCLK				10	MHz
CLOCK Pulse Width High	tCH		45			ns
CLOCK Pulse Width Low	tCL		45			ns
CSn Low to CLOCK High Setup	tCSS0		45			ns
CSn High to CLOCK High Setup	tCSS1		45			ns
CLOCK High to CSn Low Hold	tCSH0		30			ns
CLOCK High to CSn High Hold	tCSH1		45			ns
DATA to CLOCK High Setup	tDS		40			ns
DATA to CLOCK High Hold	tDH		0			ns
VDD High to CSn Low (power-up delay)				20		µs

Bandpass Filters with Serial Drivers SD Series: Mini Profile Filter (0º C to +65º C)

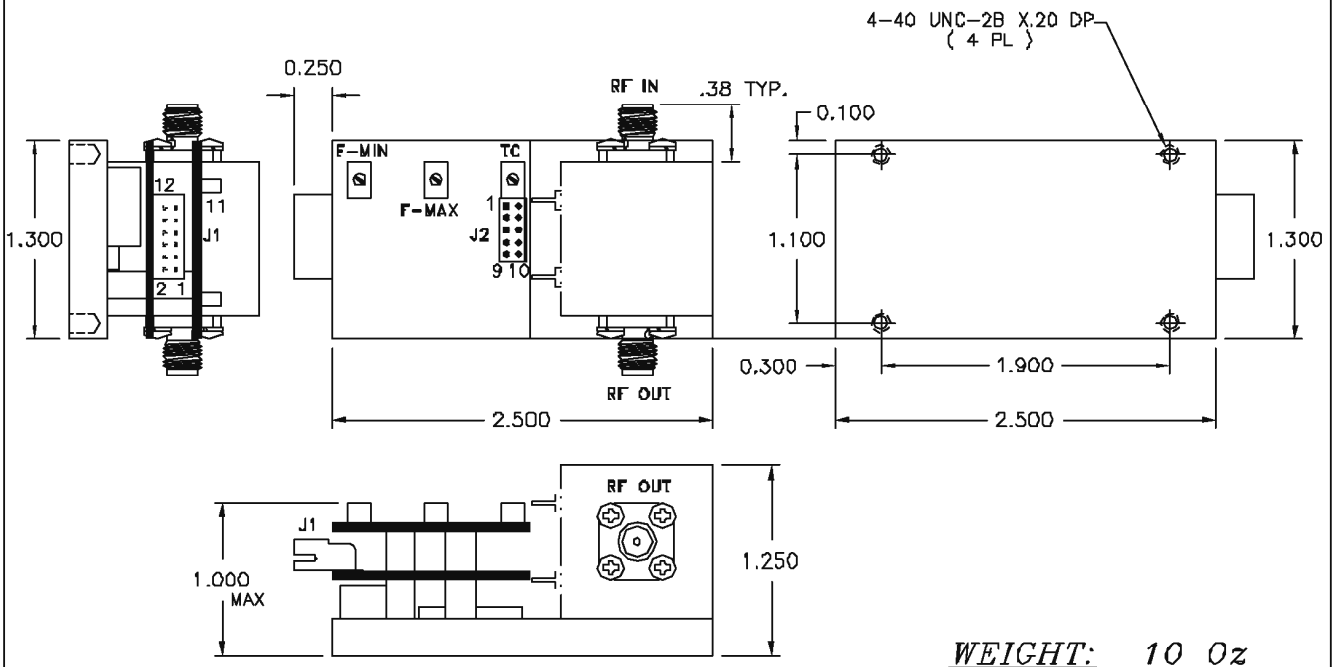
MODEL NUMBER	# Stages	Frequency GHz	3 dB Bandwidth (MHz)	Accuracy (MHz) *	Current +15V (mA)	Current -15V (mA)	Outline Drawing
MLFI-41002SD	4	1.0 to 2.0	20	+/- 6	150	50	**
MLFI-42004SD	4	2.0 to 4.0	30	+/- 8	250	50	**
MLFI-44008SD	4	4.0 to 8.0	40	+/- 12	450	50	**
MLFI-42008SD	4	2.0 to 8.0	30	+/- 13	450	50	**
MLFI-61002SD	6	1.0 to 2.0	25	+/-6	150	50	**
MLFI-62004SD	6	2.0 to 4.0	40	+/- 8	250	50	**
MLFI-64008SD	6	4.0 to 8.0	45	+/- 12	450	50	**
MLFI-62008SD	6	2.0 to 8.0	40	+/- 13	450	50	**

* Accuracy includes frequency drift and linearity errors over the temperature range.
** Contact Factory for outline Drawing.

Bandpass Filters with Serial Drivers SD Series: 1” Cube Filter (0º C to +65º C)

MODEL NUMBER	# Stages	Frequency GHz	3 dB Bandwidth (MHz)	Accuracy (MHz) *	Current +15V (mA)	Current -15V (mA)	Outline Drawing
MLFM-30520SD	3	0.5 to 2.0	15	+/- 7	150	50	**
MLFM-40540SD	4	0.5 to 4.0	15	+/- 10	250	50	21-064
MLFM-42008SD	4	2.0 to 8.0	30	+/- 13	450	50	21-064
MLFM-42018SD	4	2.0 to 18.0	40	+/- 13	1010	50	21-064
MLFM-46018SD	4	6.0 to 18.0	40	+/- 13	980	50	21-064

* Accuracy includes frequency drift and linearity errors over the temperature range.
** Contact Factory for outline Drawing.



WEIGHT: 10 Oz

BOTTOM BOARD (DAC BOARD)
J1 (2MM DUAL ROW TERMINAL STRIP)

DIGIKEY PART # : H2065-ND
MATING WITH # : H2141-ND

PIN	*FUNCTIONS*	
1	CLOCK	
2	DATA	
3	CSELECTn	
4	GROUND	
5	-V SUPPLY	
6	+V SUPPLY	
7	HEATER 1	
8	HEATER 2	
9	FM +	(*)
10	FM -	(*)
11	N/C	
12	N/C	

NOTES:

1- (*) : NOT USED FOR FILTER

2- RECOMMENDED WIRE SIZE = 20-22 GAUGE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCE ARE : FRACTIONS ± DECIMALS .XX ±.010 .XXX ±.005 ANGLES ±	CONTRACT NO.	MICRO LAMBDA WIRELESS, INC.		
	APPROVALS / DATE			
MATERIAL	DRAWN N.NGUYEN 4/11/02	*1" FILTER W/ 1.3" 16 BIT SERIAL DIGITAL DRIVER*		
FINISH	CHECKED			
DO NOT SCALE DRAWING	ISSUED	SIZE:	CAGE No: 0RN63	DWG. NO: 21 - 064 REV: A